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<b>Application Number</b>	<b>10/734,117</b>
<b>Filing Date</b>	<b>December 15, 2003</b>
<b>First Named Inventor</b>	<b>YELLIN, Daniel</b>
<b>Group Art Unit</b>	<b>2817</b>
<b>Examiner Name</b>	<b>Not yet known</b>
<b>Attorney Docket Number</b>	<b>P-6063-US</b>

**(use as many sheets as necessary)**

Sheet	1	of	4	Attorney Docket Number	P-6063-US
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Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
FA	A	5,012,200		Melnzer	04-30-1991	
	B	5,862,460		Rich	01-19-1999	
	C	5,903,854		Abe et al.	05-11-1999	
	D	6,330,455		Ichihara	12-11-2001	
	E	5,302,914		Arntz et al.	04-12-1994	
	F	6,285,251		Dent et al.	09-04-2001	
	G	5,894,093		DaSilva et al.	12-02-1997	
	H	5,854,571		Pinckley et al.	12-29-1998	
	I	5,872,481		Sevic et al.	02-16-1999	
	J	6,133,788		Dent	10-17-2000	
	K	6,201,452		Dent et al.	03-13-2001	
	KA	5,345,189		Hornak et al.	09-06-1994	
	M	5,264,807		Okubo et al.	11-23-1993	
	N	5,809,643		Alhara	06-01-1999	
	O	5,928,702		Myers et al.	09-27-1999	
	P	5,768,269 <sup>A</sup>		Wu	05-26-1998	
	Q	5,786,727		Sigmon	07-28-1998	
	R	5,886,876		Long	03-23-1999	
	S	5,880,633		Leizerovich et al.	03-09-1999	
FA	T	5,739,723		Sigmon et al.	04-14-1998	

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## U.S. PATENT DOCUMENTS

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FA	V	5,872,481		Sevic et al.	02-16-1999	
	W	5,661,434		Brozovich et al.	08-26-1997	
	X	5,420,541		Upton et al.	05-30-1995	
	Y	5,568,094		Bowen et al.	10-22-1996	
	Z	5,548,246		Tamamoto et al.	08-20-1996	
	AA	5,541,554		Stengel et al.	07-30-1996	
	AB	5,724,005		Chen et al.	03-03-1998	
	AC	4,439,744		Kumar et al.	03-27-1984	
	AD	5,621,351		Purl et al.	04-15-1997	
	AE	4,433,312		Kahn	02-21-1984	
	AF	5,974,041		Kornfeld et al.	10-26-1999	
	AG	3,777,275		Cox	12-04-1973	
	AH	6,587,511		Barak et al.	07-01-2003	
	AI	5,901,346		Stengel et al	05-04-1999	
	AJ	2003/0125065		Hasson et al.	07-03-2003	
	AK	6,825,719		Barak et al.	11-30-2004	
	AL	10/131,750		Barak et al.	04-24-2002 (filing date)	
	AM	5,786,728		Allinikula	07-28-1998	
	AN	6,633,199		Nielsen et al.	10-14-2003	
	AO	6,147,653		Wallace et al.	11-14-2000	
	AP	5,295,162		Zarembowitch	03-15-1994	
FA	AQ	6,271,782		Steensgaard-Madsen	08-07-2001	

## FOREIGN PATENT DOCUMENTS

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Date Considered

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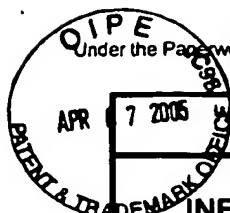
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				Application Number	10/734,117
				Filing Date	December 15, 2003
				First Named Inventor	YELLIN, Daniel
				Group Art Unit	2817
(use as many sheets as necessary)				Examiner Name	Not yet known
Sheet	3	of	4	Attorney Docket Number	P-6063-US

NON PATENT LITERATURE DOCUMENTS					
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FA ↓ V	AR	FREDRICK H. RAAB, "Efficiency of Outphasing RF Power-Amplifier Systems", IEEE Transactions on Communications. Vol. Com-33, No. 10, Oct 1985, pp. 1094-1099	<input type="checkbox"/>		
	AS	STEVE C. CRIPPS, "RF Power Amplifiers for Wireless Communications", 1999, USA, pp. 219-250	<input type="checkbox"/>		
	AT	D.C. Cox, "Linear Amplification with Nonlinear Components", IEEE Transaction on Communications, December 1974, pp.1942-1945.	<input type="checkbox"/>		
	AU	Bob Stengel and William R. Eisenstadt, "LINC Power Amplifier Combiner Method Efficiency Optimization", IEEE Transaction on Vehicular Technology. Vol.49, No. 1, January 2000, pp.229-234	<input type="checkbox"/>		
	AV	Bob Stengel, "LINC Linear Transmitter Technology Review", Presented at International Microwave Symposium, Boston, MA, June 11, 2000, pp.1-32.	<input type="checkbox"/>		
	AW	S.A. HETZEL et al., "A LINC Transmitter", University of Bristol, IEEE, 1999, pp. 133-137	<input type="checkbox"/>		
	AX	S. O. Ampem-Darko, H.S. Al-Raweshidy, "A Novel Technique for Gain/Phase Error Cancellation in LINC Transmitters", 1999 IEEE, pp. 2034-2038	<input type="checkbox"/>		
FA	AY	T. A. D. Riley and M. A. Copeland, "A simplified continuous phase modulator technique," IEEE Trans. on circuits and systems, VOL. 41, NO. 5, pp. 321-328, May 1994.	<input type="checkbox"/>		

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Substitute for form 1449B/PTO				<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				Application Number	10/734,117
				Filing Date	December 15, 2003
				First Named Inventor	YELLIN, Daniel
				Group Art Unit	2817
				Examiner Name	Not yet known
Sheet	4	of	4	Attorney Docket Number	P-6063-US

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FA ↓ V	AZ	N. M. Filiol, T. A. D. Riley, C. Plett and M. A. Copeland, "An agile ISM band frequency synthesizer with built-in GMSK data modulation," IEEE Journal of Solid-State Circuits, VOL. 33, NO. 7, pp.998-1008, July 1998.	<input type="checkbox"/>
	BA	T. Yamaji, A. Yasuda, H. Tanimoto, Y. Suzuki, "A Digital-to-RF Converter Architecture Suitable for a Digital-to-RF Direct-Conversion Software Defined Radio Transmitter", IEICE Trans. Commun., vol. E83-B, No. 6, pp. 1254-1260, June 2000.	<input type="checkbox"/>
	BB	J. Nieznanski, "Performance characterization of vector sigma-delta modulation", IECON '98, Proceedings of the 24th Annual Conference of the IEEE, vol. 1, pp. 531-536, 1998	<input type="checkbox"/>
	BC	ZHANG et al., "Multibit Oversampled Sigma - Delta A/D Converter with Nonuniform Quantisation", ELECTRONICS LETTERS, 14 MARCH 1991, UK, Vol. 27, No. 6, pp. 528-529	<input type="checkbox"/>
	BD	D.L. Neuhoff, "Polar quantization revisited", IEEE International Symposium on Information Theory, 1997 Proceedings, pp. 60, 1997	<input type="checkbox"/>
FA	BE	N.O. Sokal, "RF Power Amplifiers, Classes A through S – How they operate, and when to use them", Electronics Industries Forum of New England, Professional Program Proceedings, pp. 179-183, 1997	<input type="checkbox"/>
			<input type="checkbox"/>

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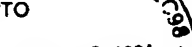
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			Application Number	10/734,117	
			Filing Date	December 15, 2003	
			First Named Inventor	YELLIN, Daniel	
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			Examiner Name	Not yet known	
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Sheet	1	of	2		

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	10/734,117
		Filing Date	December 15, 2003
		First Named Inventor	YELLIN, Daniel
		Group Art Unit	2817
		Examiner Name	Not yet known
Sheet 2 of 2	Attorney Docket Number	P-6063-US	

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FA	B	Perrott et al., "A 27-MW CMOS Fractional-N Synthesizer Using Digital Compensation for 2.5-MB/S GFSK Modulation", IEEE Journal of Solid-State Circuits, IEEE Inc., New York, USA, Vol. 32, No. 12, December 1, 1997, pp. 2048-2080	<input type="checkbox"/>
FA			
FA	C	M.H. Perrott, "Techniques for High Data Rate modulation and Low Power Operation of Fractional-N Frequency Synthesizers", PH.D Dissertation, MIT, September 1997, Retrieved from the Internet, URL: <a href="http://www-mtl.mit.edu/perrott/pages/thesis.pdf">http://www-mtl.mit.edu/perrott/pages/thesis.pdf</a> , pp. 1-80	<input type="checkbox"/>
FA	D	International Search Report from PCT/US2004/010959, mailed on October 7, 2004	<input type="checkbox"/>
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				First Named Inventor	YELLIN, Daniel
				Group Art Unit	Not yet known
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Sheet	1	of	2		

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FA	B	MICHAEL H. PERROTT ET AL., "A Modeling Approach for $\Sigma$ - $\Delta$ Fractional-N Frequency Synthesizers Allowing Straightforward Noise Analysis", IEEE Journal of Solid-State Circuits, Vol. 37, No. 8, August 2002, pp. 1028-1038	<input type="checkbox"/>	
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